## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application No.:

09/912,745

Filed:

07/24/2001

Inventor(s):

Richard A. Zartorski

JUL 1 9 2004

Title:

Bus Bridge Circuit

Including Audio Logic and an Addressable Register for Storing an Address Bit Used

When the Audio Logic Accesses Digital Data, and Method for Initializing a Chip Set Including the Bus

**Bridge Circuit** 

Examiner:

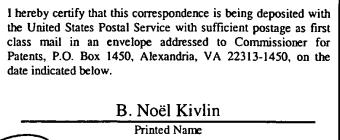
Lefkowitz, Sumati

Group/Art Unit:

2112

Atty. Dkt. No:

5681-46700



Printed Name

7 - 15 - 04

Signature

Date

## RESPONSE TO OFFICE ACTION OF April 22, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of April 22, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.